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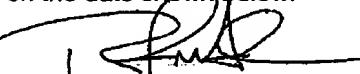
JAN 07 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of
 Lasserre Docket Number: TI-31354
 Serial No.: 09/932,381 Art Unit: 2188
 Filed: August 17, 2001 Examiner: Bragdon
 For: Local Memory With Indicator Bits to Support Concurrent DMA and
 CPU Access

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 NEW APPLICATION
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 CONTINUATION APP'N
 DIVISIONAL APP'N

**SECOND AMENDMENT UNDER
 RULE 116 (8 Pages)**
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RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2188

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Serge Lasserre (TI-31354)

Conf. No.: 4434

Serial No: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Art Unit: 2188

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

SECOND AMENDMENT UNDER RULE 116

Introductory Comments:

This paper is presented in response to the Office Action mailed on October 7, 2004, and in response to the Advisory Action mailed December 27, 2004. Entry of this amendment in this application, and reconsideration of the final rejection of claims in this application, are respectfully requested.

Proposed amendment to the claims begin on page 2.

Applicant's remarks begin on page 7.